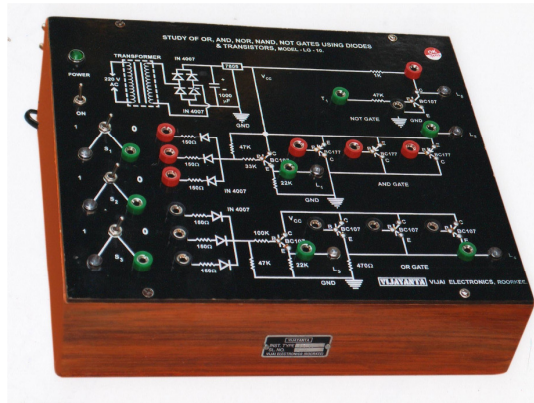# STUDY OF OR, NOR, AND, NAND, NOT GATES USING DIODES AND TRANSISTOR,
MODEL – LG – 10.

FEATURES:

** Regulated short circuit proof + 5, Volt power supply suitable to this experimental board is builtin.

** Circuit is printed and components are mounted on the top of the painted sunmica aluminum to give better and clear understanding.

** A complete working manual with theory, circuit details and operating instruction supplied with the experimental board.

** Stackable type connecting leads suitable to the terminals are supplied with the board for easy inter – connections and longer life of the terminals.

** Outputs of the gates are displayed by LED's.

** To test the logic gates, three test sources are builtin which give 0 and 1 conditions by the toggle switches, mounted on panel.

EXPERIMENTS :

1. To study the principle of OR Gate for two and three inputs.
2. To study the principle of NOR Gate for two and three inputs.
3. To study the principle of AND Gate for two and three inputs.
4. To study the principle of NAND Gate for two and three inputs.
5. To study the principle of NOT circuit.

OTHER APPARATUS REQUIRED :

1. Board is self sufficient.

There may be any change in specification due to continuous R&D without any notice