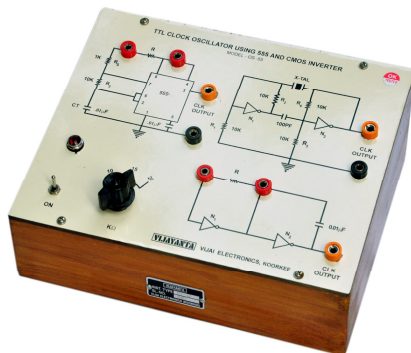


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STUDY OF CLOCK OSCILLATOR USING 555, **CMOS INVERTER, MODEL - OS - 55.**



FEATURES :

- ** This experimental board model – OS - 555 is designed to study of a clock oscillator using timer I.C. – 555 and CMOS Inverter.
- ** Circuit is printed on the top of the painted aluminum sheet to facilitate better and clear understanding.
- ** A prewired selector three range switch (Resistance) are provide to obtain different charge time.
- ** A complete working manual containing theory, circuit details and operating instruction supplied with the experimental board.
- ** Builtin Regulated and short circuit proof ± 5 Volt D.C. Power Supply suitable to the experimental board.
- ** Stackable type connecting leads suitable to the terminals are supplied with the board for easy inter – connections and longer life of the terminals.

EXPERIMENTS :

1. The 555 Clock Oscillator.
2. The Clock Generator by CMOS Inverter Astable.

OTHER APPARATUS REQUIRED :

1. A General purpose Dual Trace C.R.O.