

VIJAYANTA

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EXPERIMENTAL BOARD ON 4 – BIT SHIFT REGISTERS, MODEL – SR – 04.



This experimental board offers a facilities of studying a FOUR BIT SHIFT REGISTERS. To perform this experiment the various facilities available on the board are :

1. Four Master Slave J – K Flip – Flop, cone inverter and a contact bounce free manual clock.
2. A CLEAR key is provided for clearing the Flip – Flops from previous data. The output of the Flip – Flops can be seen by the LED attached to it therefore, no apparatus is required for testing the Shift Register.
3. A DATA terminal with a switch is also available on the board. This terminal can be used for feeding data into a circuit. The switch connects a 1 or 0 at this terminal. A fully regulated and short circuit proof + 5, Volt power supply is builtin.

A ON / OFF switch with indicating lamp is provided. As soon as this switch is put to ON position, the DC supply to different Flip – Flops gets connect automatically. The layout on the board is quite spread out to facilitate convenient working and clear understanding. Stackable type patch cords (connecting leads) suitable to the terminals are supplied with the board for easy and perfect inter connections and longer working life the terminals.